

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

En-Hsing Chen, et al.

Title:

NAND MEMORY ARRAY INCORPORATING MULTIPLE SERIES SELECTION DEVICES AND METHOD FOR OPERATION OF SAME

Application No.: 10/729,865

Filed:

December 5, 2003

Examiner:

Not Yet Assigned

Group Art Unit: 2818

NOA Date:

N/A

Issue Batch:

N/A

Atty. Docket No.: 023-0029

March 30, 2004

Mail Stop Missing Parts COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, VA 22313-1450

SUBMISSION OF NEW FORMAL DRAWINGS

Transmitted herewith are 17 sheets of new drawings containing Fig. 1; Fig. 2; Fig. 3; Fig. 4 and Fig. 5; Fig. 6 and Fig. 7; Fig. 8; Fig. 9; Fig. 10; Fig. 11; Fig. 12; Fig. 13; Fig. 14; Fig. 15; Fig. 16; Fig. 17A, Fig. 17B and Fig. 17C; Fig. 17D and Fig. 17E; and Fig. 18, respectively.

These formal drawings are being submitted prior to the first Office Action on the merits to facilitate the Examiner's review, and in hopes that the Official Draftsperson will review these formal drawings rather than the informal drawings submitted with the application as filed.

CERTIFICATE OF MAILING OR TRANSMISSION
I hereby certify that, on the date shown below, this correspondence is being
deposited with the US Postal Service with sufficient postage as first class mail, in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450. facsimile transmitted to the US Patent and Trademark Office. Andrew C. Graham Date

Respectfully submitted

Andrew C. Graham, Reg. No. 36,531 Attorney for Applicant(s)

(512) 338-6313 (512) 338-6301 (fax)

EXPRESS MAIL LABEL: